

FIG. 1
(PRIOR ART)

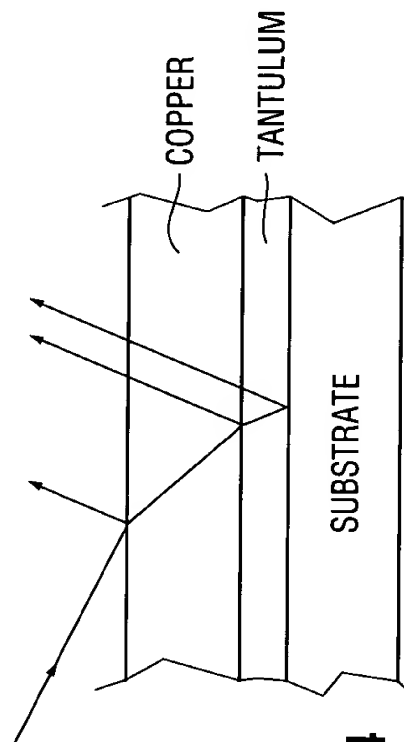


FIG. 4

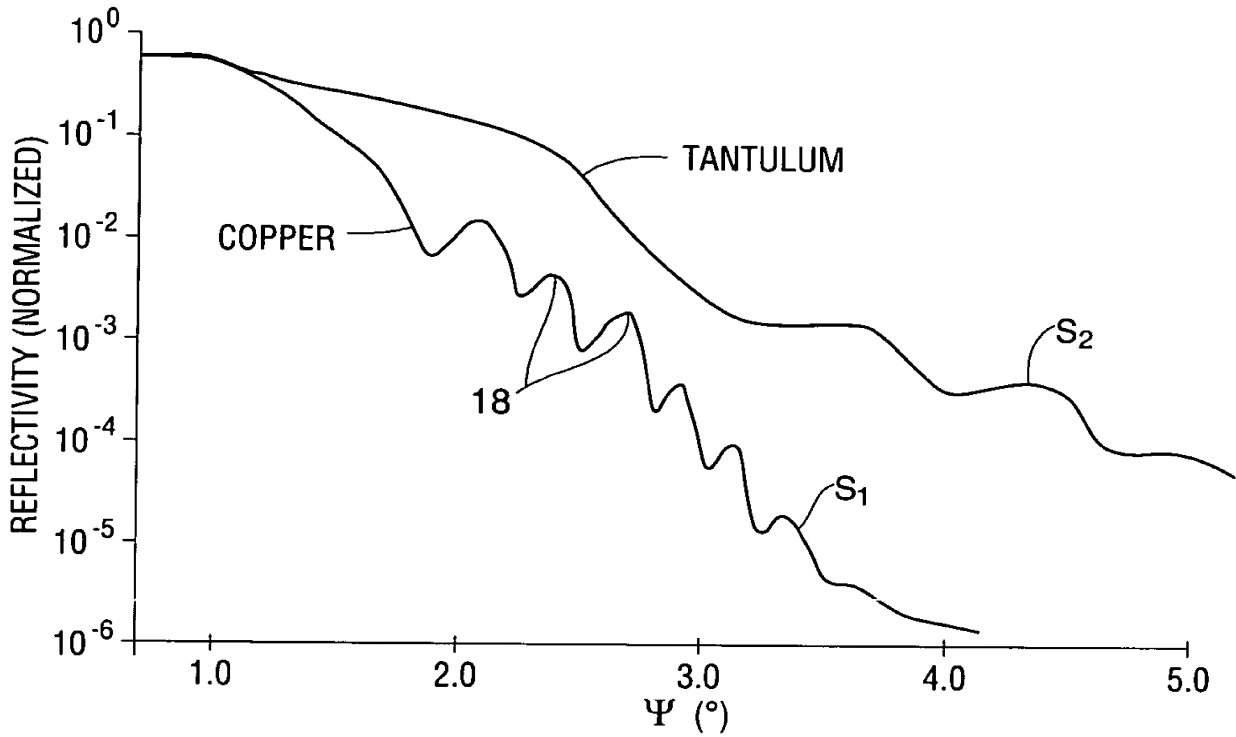


FIG. 2
(PRIOR ART)

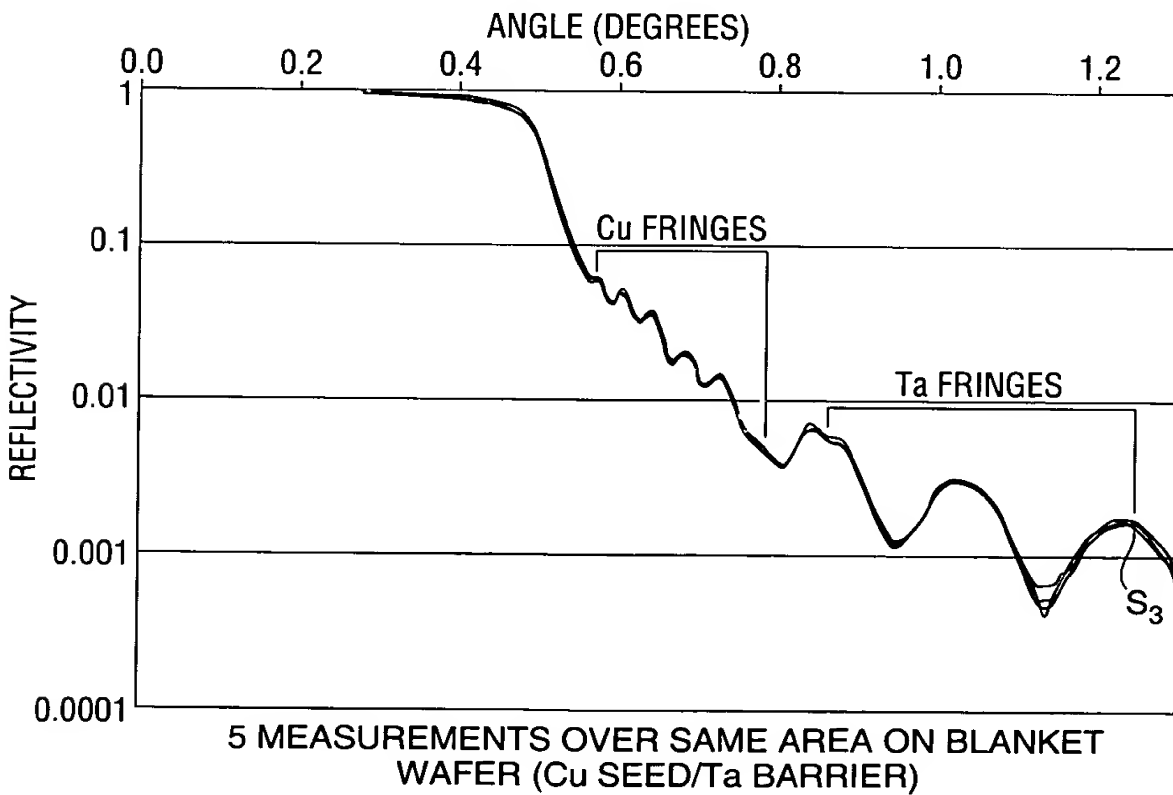


FIG. 3

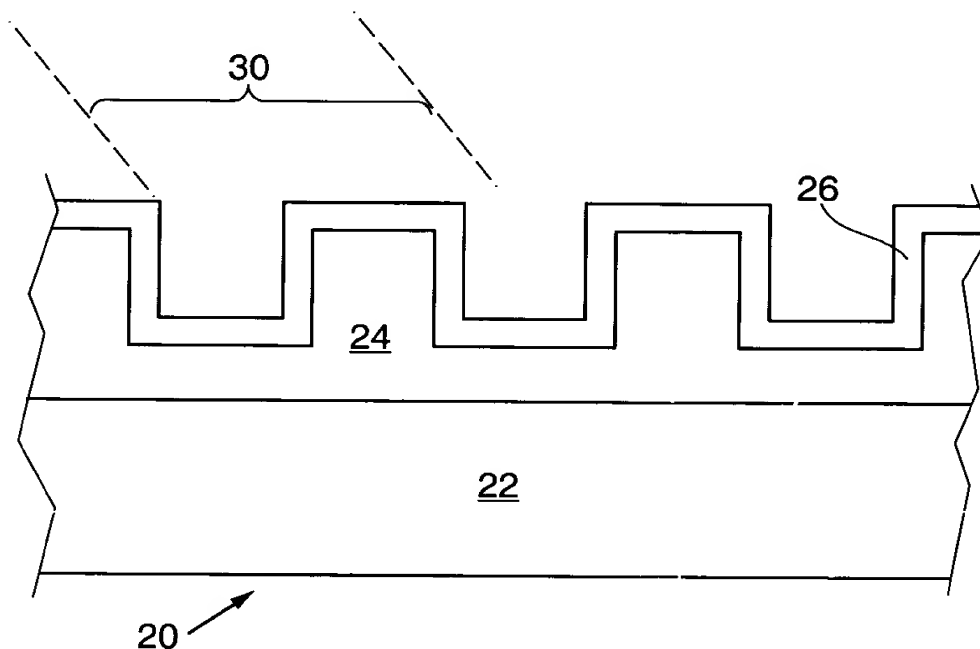
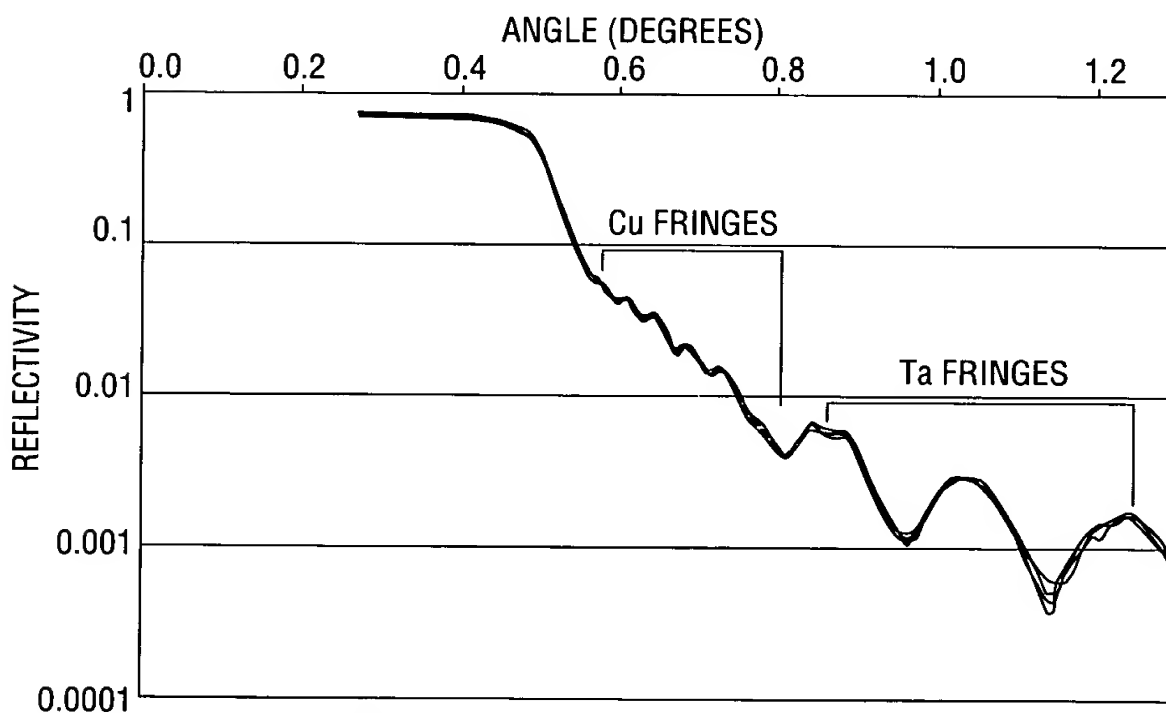


FIG. 5



5 MEASUREMENTS OVER SAME AREA ON PATTERNED
 WAFER (Cu SEED/Ta BARRIER)

FIG. 6